(310) 207-3800

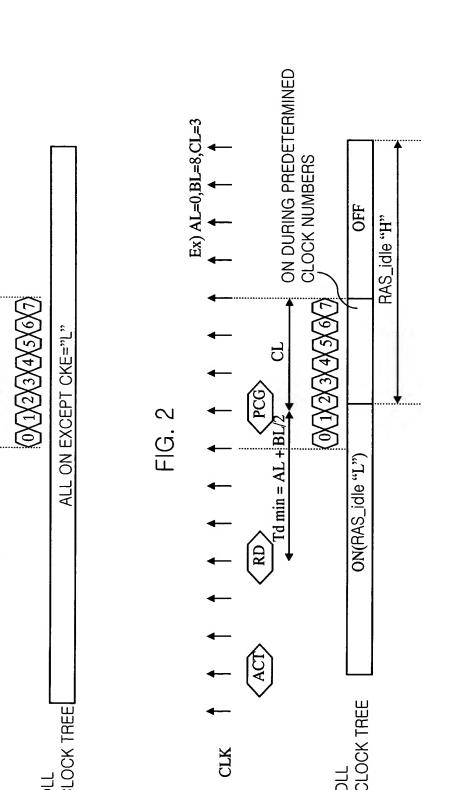
Docket No.: 51876P366

Blakely, Sokoloff, Taylor & Zafman LLP (310) 207-380 Title: synchronous semiconductor memory device having clock synchronization circuit and circuit for controlling on/off of clock tree of the clock synchronization circuit

1st Named Inventor: IHL-HO LEE Express Mail No.: EV339918145US

Sheet: 1 of 5

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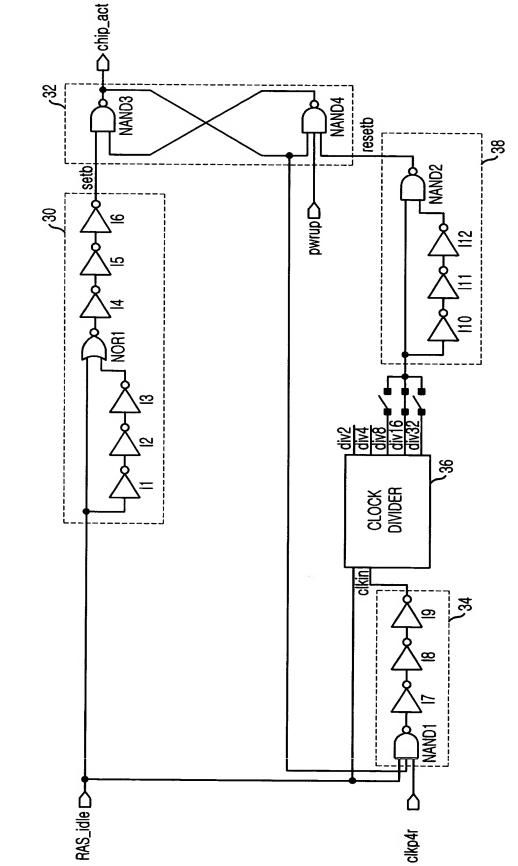
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Title: SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE HAVING CLOCK
SYNCHRONIZATION CIRCUIT AND CIRCUIT FOR CONTROLLING ON/OFF OF CLOCK TREE OF
THE CLOCK SYNCHRONIZATION CIRCUIT
1st Named Inventor: IHL-HO LEE
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Docket No.: 51876P366
Sheet: 2 of 5

Docket No.: 51876P366



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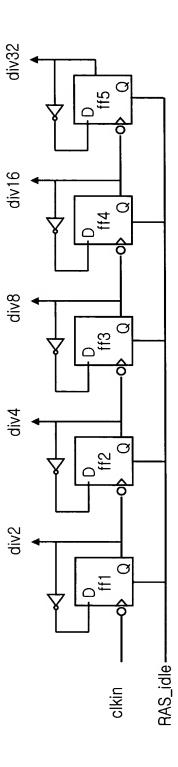
Blakely, Sokoloff, Taylor & Zafman LLP

Title: SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE HAVING CLOCK
SYNCHRONIZATION CIRCUIT AND CIRCUIT FOR CONTROLLING ON/OFF OF CLOCK TREE OF
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Docket No.: 51876P36
Sheet: 3 of 5

Docket No.: 51876P366





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SYNCHRONIZATION CIRCUIT AND CIRCUIT FOR CONTROLLING ON/OFF OF CLOCK TREE OF
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1st Named Inventor: IHL-HO LEE
Express Mail No.: EV339918145US
Docket No.: 51876P366
Sheet: 4 of 5

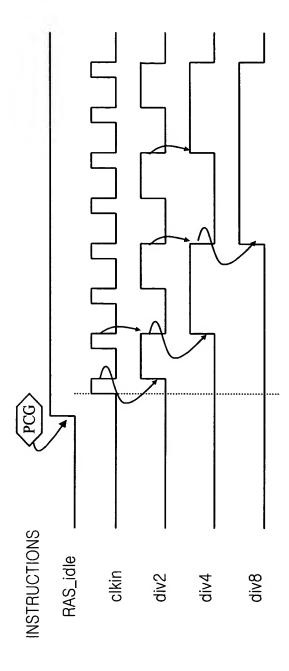


FIG. 5

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SYNCHRONIZATION CIRCUIT FOR CONTROLLING ON/OFF OF CLOCK TREE OF
THE CLOCK SYNCHRONIZATION CIRCUIT

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Docket No.: 51876P366



